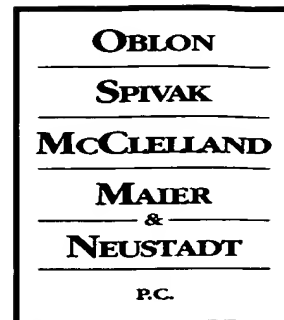




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Docket No.: 212740US2SRD CONT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 09/927,632

Applicants: Yoshiyuki MATSUNAGA, et al.

Filing Date: August 13, 2001

For: IMAGE SYSTEM, SOLID-STAGE IMAGING
SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE USED IN THE IMAGE SYSTEM, AND
DIFFERENCE OUTPUT METHOD USED FOR THE
IMAGE SYSTEM

Group Art Unit: 2612

Examiner: Aung Soe Moe

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SIR:

Attached hereto for filing are the following papers:

RESPONSE TO RESTRICTION REQUIREMENT

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
YOSHIYUKI MATSUNAGA, ET AL. : EXAMINER: AUNG SOE MOE
SERIAL NO: 09/927,632 :
FILED: AUGUST 13, 2001 : GROUP ART UNIT: 2612
FOR: IMAGE SYSTEM, SOLID-STAGE :
IMAGING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE USED
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DIFFERENCE OUTPUT METHOD USED
FOR THE IMAGE SYSTEM

RESPONSE TO RESTRICTION REQUIREMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the restriction requirement of August 20, 2004, Applicants elect the Species of Figs. 35-36, corresponding to claims 7, 37, 38, 67-80 and 87. Examination on the merits is in order, and an early and favorable decision is earnestly solicited.

Respectfully submitted,

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